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-	2525	double adj precision	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/13 08:28
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-	129	708/603.ccls. and @ad<19990120	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/13 13:29
-	1	("6293874").PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/13 13:29
-	1	long adj product adj sum	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:11
-	245	processor and crypto$6 and rsa and "elliptic curve"	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:11
-	14	processor and crypto$6 and rsa and "elliptic curve" and adder and multiplier and ((finite galois) adj field) and integer	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:26
-	4	@ad<19990120 and processor and crypto$6 and rsa and "elliptic curve" and adder and multiplier and ((finite galois) adj field) and integer	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:46
-	3	@ad<19990120 and processor and crypto$6 and rsa and "elliptic curve" and adder and multiplier and ((finite galois) adj field) and integer and (carry$3 carries) and propagat$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 15:20
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–	1	jp-09311854-a.did.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/19 08:40
–	1	("4745568").PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/19 08:41
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–	*abstracts* 75	@ad<19990120 and ("finite field" galois "GF(2.sup.m)" "GF(2^m)") and (modular adj (multiplication exponentiation))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 08:43
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–	1	@ad<19990120 and 708/603.ccls. and "modular multiplication"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:59
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–	129	708/603.ccls. and @ad<19990120	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/19 15:04
–	101	(708/603.ccls. and @ad<19990120) and adder and multiplier	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/19 15:25
–	*abstracts + kwic* 10	((708/603.ccls. and @ad<19990120) and adder and multiplier) and ((single double) adj precision)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/20 07:32

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... Optional Exercise: Draw a complete logic circuit for the **full adder** using
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Philips Semiconductors - Catalog - **Full adders**
... Product info on 74F283 74F283, 4-bit binary **full adder** with fast carry. **Gates** -
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...Innocent as they look here, **full-adders** are in fact built up ... themselves can be built
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